

GK-US035197

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	
Min-Su JEONG	:	Patent Art Unit: 2611
Serial No.: 10/724,151	:	Examiner: Patel, Jayanti K
Filed: December 1, 2003	:	
For: PHASE LOCKED LOOP FREQUENCY SYNTHESIZER WHERE FREQUENCY GAIN VARIATION CONTROLLED OSCILLATOR IS COMPENSATED	:	

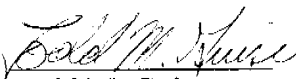
REQUEST FOR STATUS

Assistant Commissioner of Patents
Washington, DC 20231

Sir:

The above-identified application has been pending since December 1, 2003. Please let us know when the Applicant can expect a first Office Action on the above-identified application. If there are any questions regarding this Request, please feel free to contact the undersigned.

Respectfully submitted,


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